

Electronic Acknowledgement Receipt

EFS ID:	6690005
Application Number:	10719020
International Application Number:	
Confirmation Number:	9168
Title of Invention:	Method for preparing a circuit board material having a conductive base and a resistance layer
First Named Inventor/Applicant Name:	Akira Matsuda
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Filer:	Shuji Yoshizaki
Filer Authorized By:	
Attorney Docket Number:	032130
Receipt Date:	22-DEC-2009
Filing Date:	24-NOV-2003
Time Stamp:	15:49:19
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Transmittal Letter	032130_IDS_Transmittal_12_2_2009.pdf	54378 9916d67e7a2610f1c0f166c76a1261057e5a55bc0d6	no	2

Warnings:

Information:

2	Information Disclosure Statement (IDS) Filed (SB/08)	032130_IDS_SB_08a_12_22_2009.pdf	785661 1c16ea067c7c74b6789308d14c6533f1be7c	no	4
Warnings:					
Information:					
3	Foreign Reference	CN1037039.pdf	458120 9f0d7c13d6de72da18ba98b79178c1794d34d37	no	10
Warnings:					
Information:					
4	Foreign Reference	TW419530.pdf	1062682 25ab71242c98bdc070154c8db3b1f8b19a2fe72e9	no	25
Warnings:					
Information:					
5	NPL Documents	TaiwaneseOA.pdf	831072 54a51be5a7e84046212f6c8a2efce422967a0	no	6
Warnings:					
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Total Files Size (in bytes):			3191913		
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